Listing of the Claims:

This listing of claims will replace all prior versions and listings of claims in the application.

1. (Cancelled)

2. (Currently Amended) A TFT LCD (thin film transistor liquid crystal display) comprising:

a first substrate and a second substrate;

a scanning line on the first substrate;

a signal line formed to cross the scanning line, wherein the signal line does not include an extension pattern;

figs. 2A, 2B a channel layer formed along the signal line and extended to a portion of the scanning line;

source and drain electrodes formed separated on the channel layer over the scanning line;

a pixel electrode connected to the drain electrode; and

a liquid crystal layer formed between the first substrate and the second substrate;

wherein the drain electrode is parallel to the signal line and is formed to cross the scanning line.

3. (Previously Presented) A TFT LCD as claimed in claim 2, wherein the channel layer has a width smaller than a width of the signal line and a width the scanning line.

4. (Previously Presented) A TFT LCD as claimed in claim 2, further comprising a gate insulating layer between the scanning line and the channel layer.

5. (Previously Presented) A TFT LCD as claimed in claim 2, further comprising an ohmic contact layer between each of the source and drain electrodes and the channel layer.

6. (Previously Presented) A TFT LCD as claimed in claim 2, wherein the source electrode and the signal line are formed as a unit.

7. (Cancelled)

8. (Cancelled)

9. (Currently Amended) A TFT LCD comprising:

a first substrate and a second substrate;

a plurality of scanning lines on the first substrate;

a gate insulating layer on an entire surface inclusive of the scanning lines;

a channel layer on the gate insulating layer to cross the scanning lines having a portion extended to a top of at least one of the plurality of scanning lines;

source and drain electrodes formed separated on the channel layer over the scanning lines;

a signal line formed as a unit with the source electrode along the channel layer which is formed to cross the scanning lines, wherein the signal line does not include an extension pattern;

of the 1st substrate

a protection film formed on an entire surface inclusive of the signal line;

a pixel electrode connected to the drain electrode on the protection film; and[[,]]

a liquid crystal layer formed between the first substrate and the second substrate[[;]],

said at least one of

wherein the drain electrode is parallel to the signal line and is formed to cross the

lines

scanning line.

10. (Cancelled)

11. (Previously Presented) A TFT LCD as claimed in claim 9, wherein the channel

a width of said one of

layer has a width smaller than a width of the signal line and the scanning line.

lines

12. (Previously Presented) A TFT LCD as claimed in claim 9, further comprising an

each of

ohmic contact layer between the source and drain electrodes and the channel layer.

said one of

13. (Previously Presented) A TFT LCD as claimed in claim 9, wherein the scanning

lines

line has a portion enlarged in the vicinity of the signal line.

fig. 3

14. (Original) A TFT LCD as claimed in claim 13, wherein the channel

said one of

layer is formed along the signal line over the scanning line, and has a width enlarged as much as

said one of lines

a width of the scanning line is enlarged.

15. (Currently Amended) A TFT LCD having a first substrate, a second substrate, and liquid crystal sealed between the first and second substrates, comprising:

a scanning line on the first substrate;

a gate insulating layer on the scanning line;

a channel layer on the gate insulating layer;

figs. 2A, 2B

a signal line formed to cross the scanning line to cover a portion of the channel layer, wherein the signal line does not include an extension pattern;

a drain electrode formed on the channel layer spaced a distance away from the signal line in parallel to the signal line;

a protection film formed on an entire surface of the first substrate inclusive of the drain electrode; and

a pixel electrode formed on the protection film connected to the drain electrode;

13 wherein the drain electrode is parallel to the signal line and formed to cross the scanning

14 line.

16. (Original) A TFT LCD as claimed in claim 15, wherein the channel layer is formed along the signal line.

17. (Original) A TFT LCD as claimed in claim 16, wherein the channel layer has a width smaller than a width of the signal line and the scanning line.

18. (Original) A TFT LCD as claimed in claim 15, wherein the signal line serves as a source electrode disposed opposite to the drain electrode.

19. (Original) A TFT LCD as claimed in claim 15, further comprising a gate insulating layer between the scanning line and the channel layer.

20. (Original) A TFT LCD as claimed in claim 18, further comprising an ohmic contact layer between each of the source and drain electrodes and the channel layer.